

FIG. 1

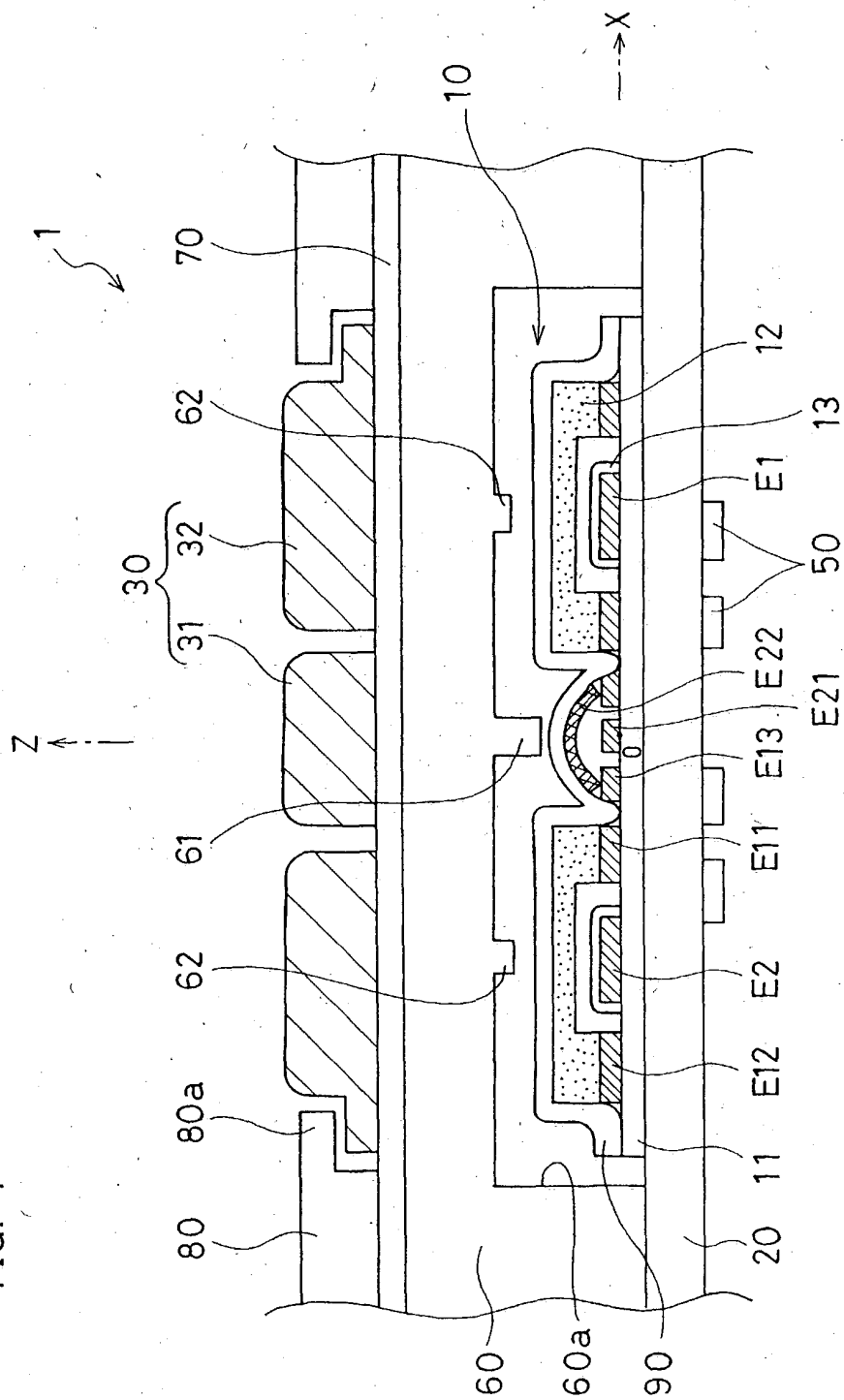


FIG. 2

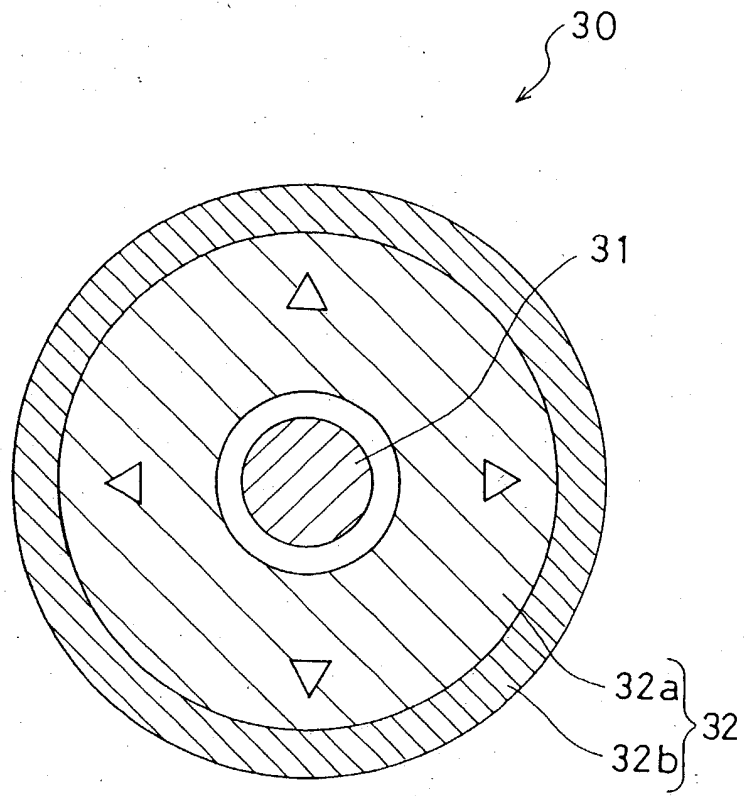


FIG. 3

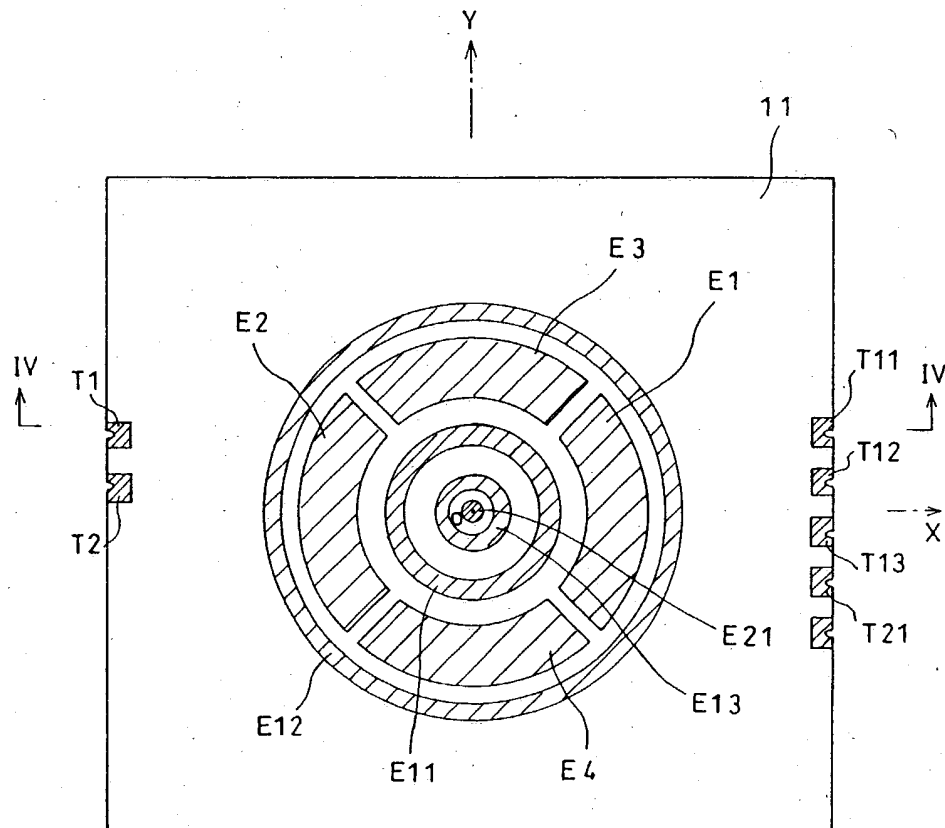


FIG. 4

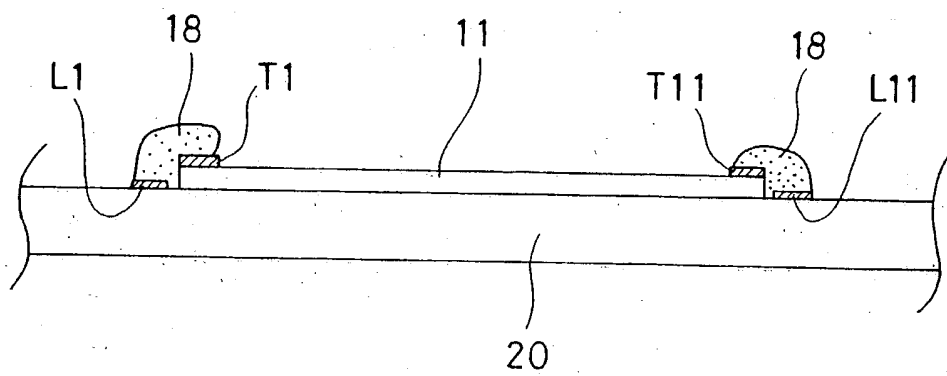


FIG. 5

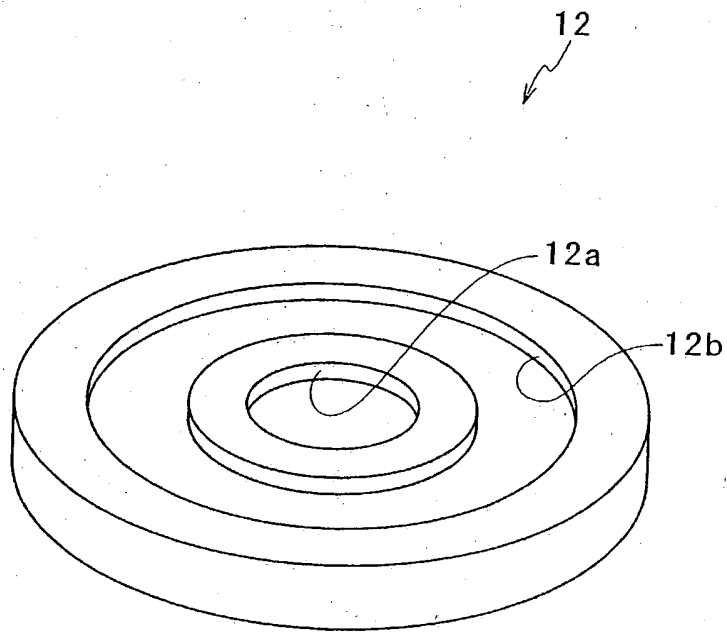
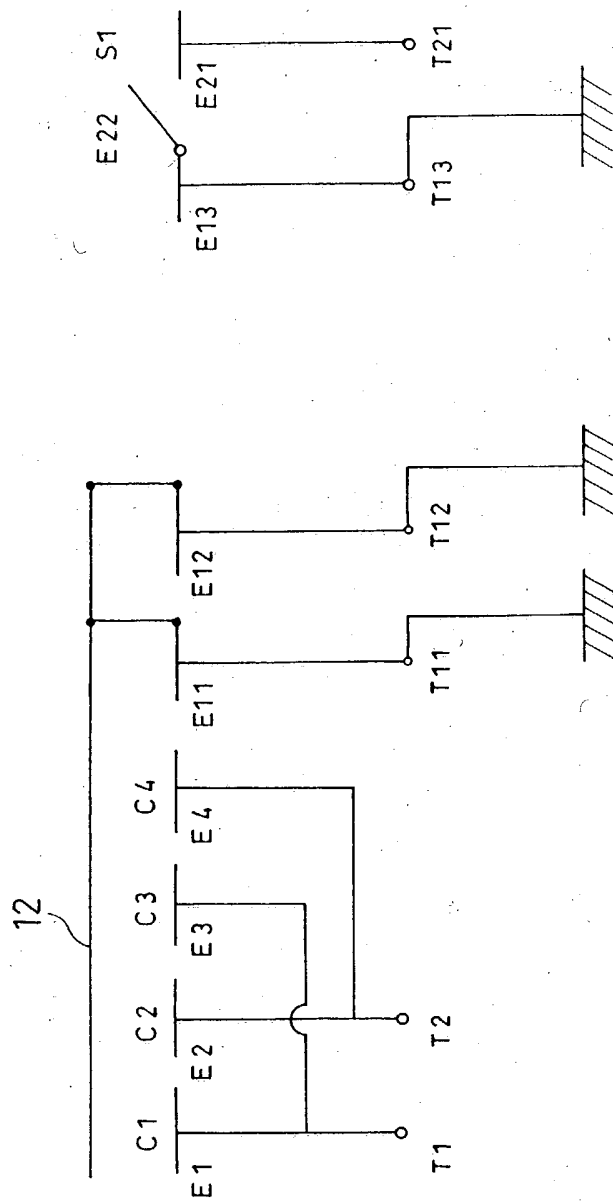


FIG. 6



The figure consists of two parts illustrating a semiconductor device structure.

- Top Part (Cross-section along Z-axis):** This part shows a vertical stack of layers. From top to bottom, there are regions labeled 80, 80a, 62, 31, 32, 61, 62, and 70. A bracket groups regions 31 and 32 under the label 30. A dashed arrow labeled  $Z^{\wedge}$  indicates the direction of the cross-section.
- Bottom Part (Planar view along X-axis):** This part shows the horizontal arrangement of the device. It features a central region 10 surrounded by various structures. Labels include 60, 60a, 90, 11, 20, E12, E2, E11, E13/E22, E21, E1, 13, 12, and 50. A dashed arrow labeled  $X$  indicates the direction of the planar view.

FIG. 8

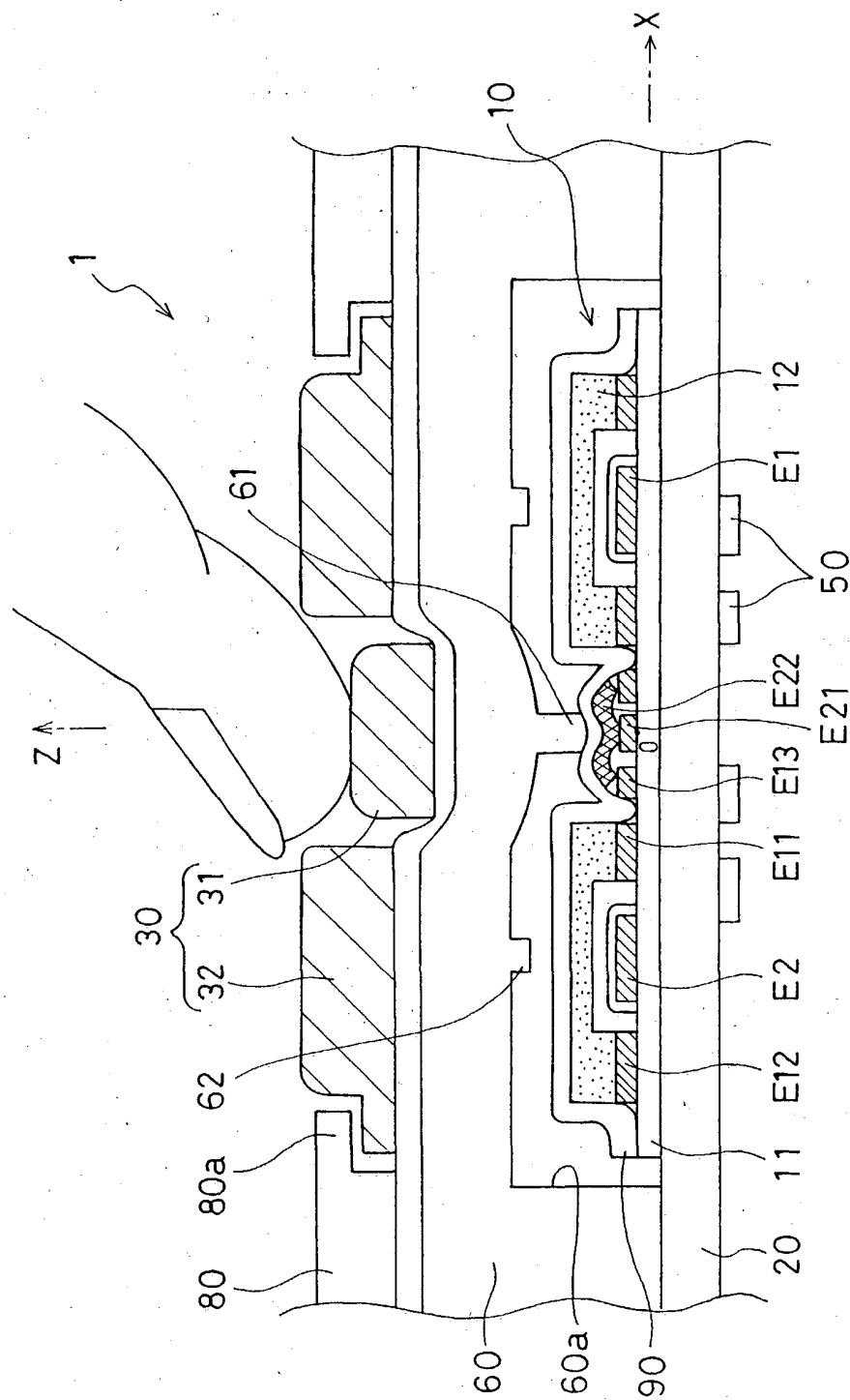




FIG. 9

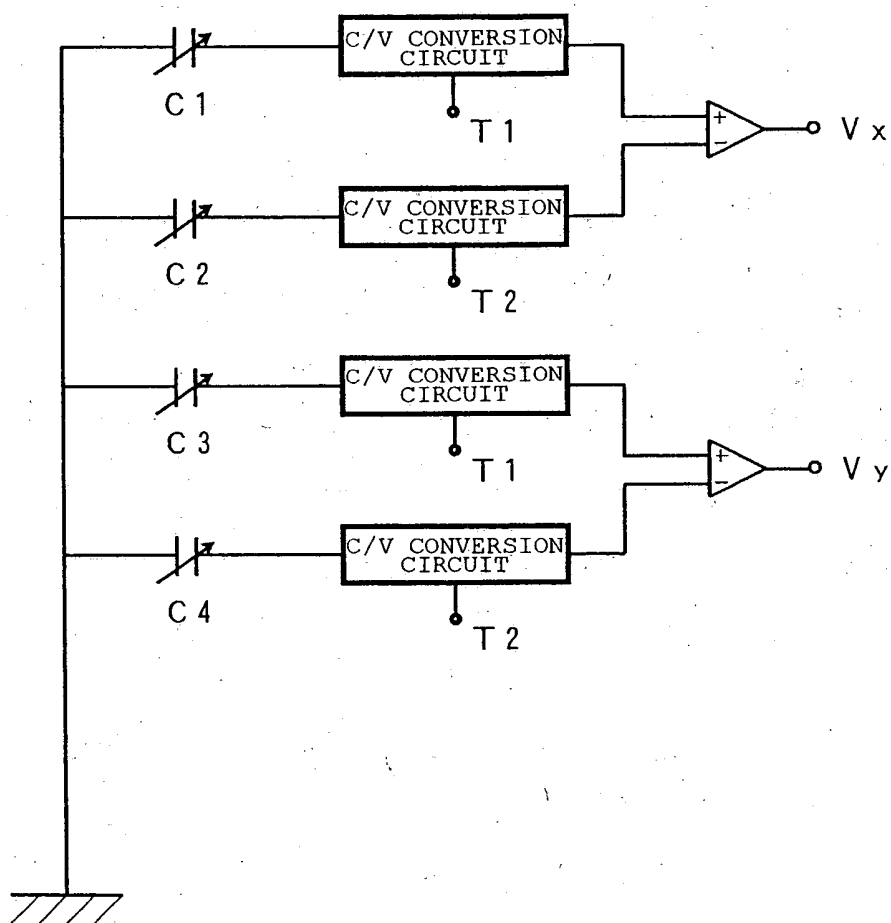


FIG. 10

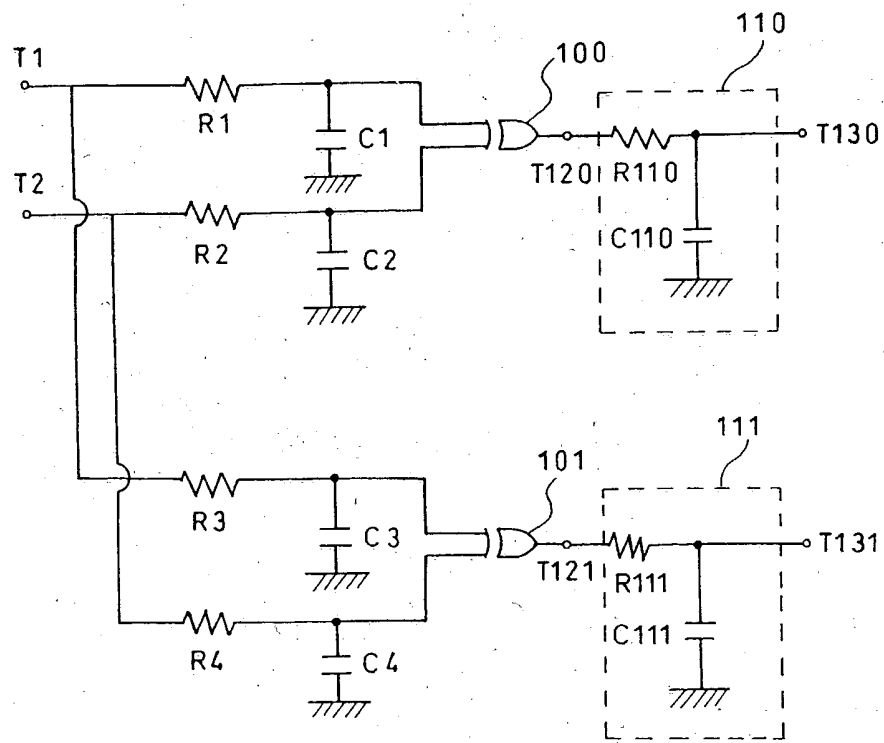


FIG. 11

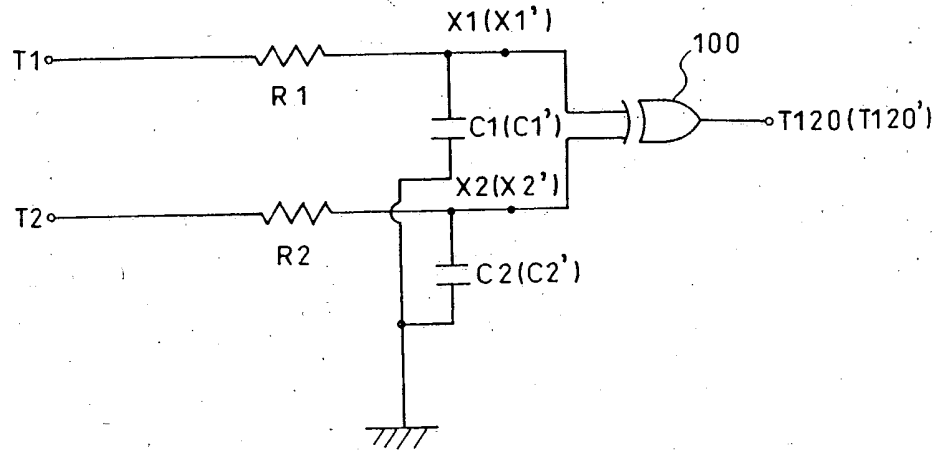


FIG. 12

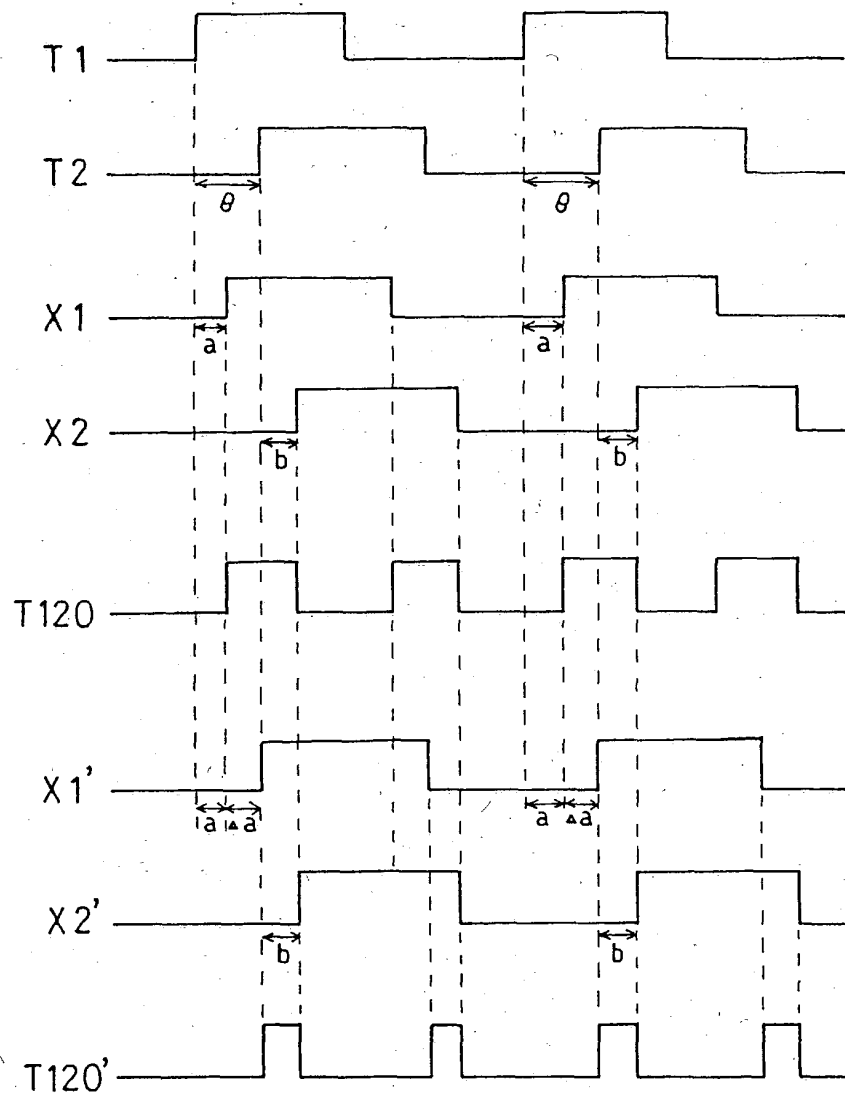


FIG. 13

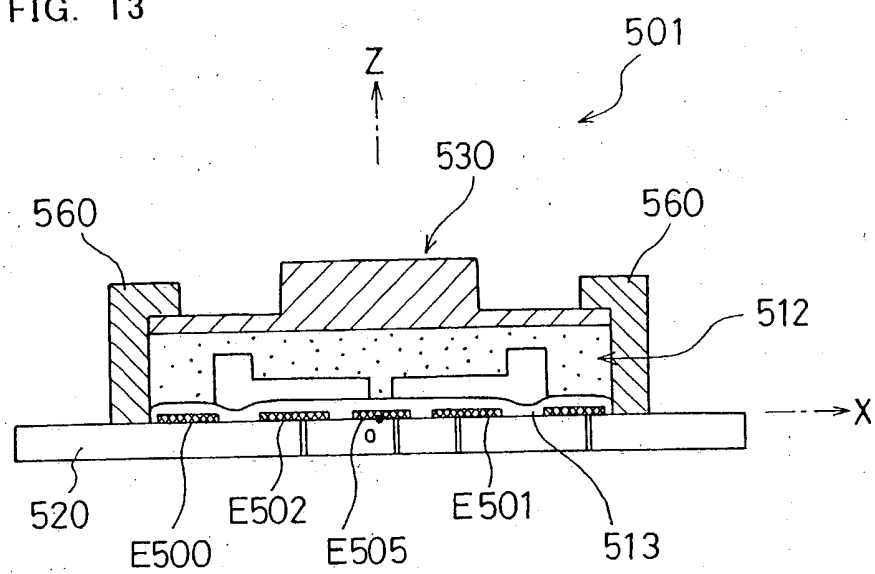


FIG. 14

